



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

2824
B793

Applicant: Leonard Forbes et al.

Title: FLASH MEMORY WITH LOW TUNNEL BARRIER INTERPOLY INSULATORS

Docket No.: 1303.014US1

Serial No.: 09/945,507

Filed: August 30, 2001

Due Date: N/A

Examiner: Son Dinh

Group Art Unit: 2824

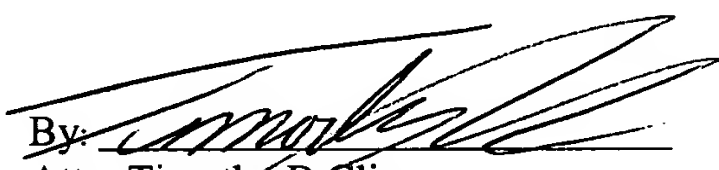
Mail Stop Amendments
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

We are transmitting herewith the following attached items (as indicated with an "X"):


- ☒ A return postcard.
- ☒ A Communication Concerning Related Applications (3 pgs.).
- ☒ An Information Disclosure Statement (1 pg.), Form 1449 (1 pg.), and copies of 7 cited documents.

Please charge any additional fees or credit overpayment to Deposit Account No. 19-0743.

SCHWEGMAN, LUNDBERG, WOESSNER & KLUTH, P.A.
Customer Number 21186

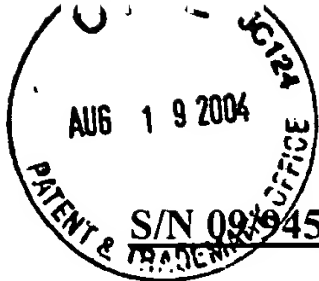
By: 
Atty: Timothy B Clise
Reg. No. 40,957

CERTIFICATE UNDER 37 CFR 1.8: The undersigned hereby certifies that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail, in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on this 11 day of August, 2004.


Name


Signature

SCHWEGMAN, LUNDBERG, WOESSNER & KLUTH, P.A.
(GENERAL)



S/N 09/945,507

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Leonard Forbes et al. Examiner: Son Dinh
Serial No.: 09/945,507 Group Art Unit: 2824
Filed: August 30, 2001 Docket: 1303.014US1
Title: FLASH MEMORY WITH LOW TUNNEL BARRIER INTERPOLY
INSULATORS

COMMUNICATION CONCERNING RELATED APPLICATION(S)

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Applicants would like to bring to the Examiner's attention the following related applications in the above-identified patent application:

<u>Serial/Patent No.</u>	<u>Filing Date</u>	<u>Attorney Docket</u>	<u>Title</u>
09/945,395 6,754,108	August 30, 2001	1303.019US1	DRAM CELLS WITH REPRESSED FLOATING GATE MEMORY, LOW TUNNEL BARRIER INTERPOLY INSULATORS
09/943,134	August 30, 2001	1303.020US1	PROGRAMMABLE ARRAY LOGIC OR MEMORY DEVICES WITH ASYMMETRICAL TUNNEL BARRIERS
09/945,512	August 30, 2001	1303.027US1	IN SERVICE PROGRAMMABLE LOGIC ARRAYS WITH LOW TUNNEL BARRIER INTERPOLY INSULATORS
09/945,554	August 30, 2001	1303.028US1	SRAM CELLS WITH REPRESSED FLOATING GATE MEMORY, LOW TUNNEL BARRIER INTERPOLY INSULATORS
09/945,500	August 30, 2001	1303.029US1	PROGRAMMABLE MEMORY ADDRESS AND DECODE CIRCUITS WITH LOW TUNNEL BARRIER INTERPOLY INSULATORS
10/075,484	February 12, 2002	1303.043US1	ASYMMETRIC BAND-GAP ENGINEERED NONVOLATILE MEMORY DEVICE

COMMUNICATION CONCERNING RELATED APPLICATIONS

Serial Number: 09/945,507

Filing Date: August 30, 2001

Title: FLASH MEMORY WITH LOW TUNNEL BARRIER INTERPOLY INSULATORS

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Dkt: 1303.014US1

10/081,818	February 20, 2002	1303.045US1	ATOMIC LAYER DEPOSITION OF METAL OXIDE AND/OR LOW ASYMMETRICAL TUNNEL BARRIER INTERPOLY INSULATORS
10/177,096	June 21, 2002	1303.063US1	GRADED COMPOSITION METAL OXIDE TUNNEL BARRIER INTERPOLY INSULATORS
10/783,695	February 20, 2004	1303.019US2	DRAM CELLS WITH REPRESSED FLOATING GATE MEMORY, LOW TUNNEL BARRIER INTERPOLY INSULATORS
10/781,035	February 18, 2004	1303.063US2	GRADED COMPOSITION METAL OXIDE TUNNEL BARRIER INTERPOLY INSULATORS
10/788,810	February 27, 2004	1303.027US2	IN SERVICE PROGRAMMABLE LOGIC ARRAYS WITH LOW TUNNEL BARRIER INTERPOLY INSULATORS
10/819,550	April 7, 2004	1303.019US3	DRAM CELLS WITH REPRESSED FLOATING GATE MEMORY, LOW TUNNEL BARRIER INTERPOLY INSULATORS
	August 10, 2004	1303.043US2	ASYMMETRIC BAND-GAP ENGINEERED NONVOLATILE MEMORY DEVICE

COMMUNICATION CONCERNING RELATED APPLICATIONS

Serial Number: 09/945,507

Filing Date: August 30, 2001

Title: FLASH MEMORY WITH LOW TUNNEL BARRIER INTERPOLY INSULATORS

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Dkt: 1303.014US1

Respectfully submitted,

LEONARD FORBES ET AL.

By Applicants' Representatives,

SCHWEGMAN, LUNDBERG, WOESSNER & KLUTH, P.A.

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Minneapolis, MN 55402

(612) 349-9587

Date

17 Aug '04

By


Timothy B. Chase

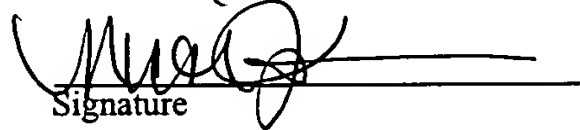
Reg. No. 40,957

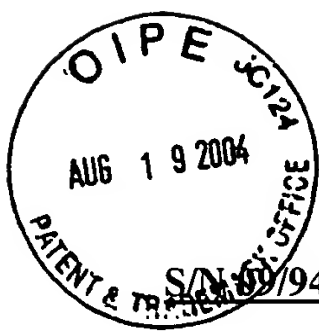
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Name

NEANE JACK

Signature





S/N 09/945,507

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:	Leonard Forbes et al.	Examiner:	Son Dinh
Serial No.:	09/945,507	Group Art Unit:	2824
Filed:	August 30, 2001	Docket:	1303.014US1
Title:	FLASH MEMORY WITH LOW TUNNEL BARRIER INTERPOLY INSULATORS		

INFORMATION DISCLOSURE STATEMENT

MS Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

In compliance with the duty imposed by 37 C.F.R. § 1.56, and in accordance with 37 C.F.R. §§ 1.97 *et. seq.*, the enclosed materials are brought to the attention of the Examiner for consideration in connection with the above-identified patent application. Applicants respectfully request that this Information Disclosure Statement be entered and the documents listed on the attached Form 1449 be considered by the Examiner and made of record. Pursuant to the provisions of MPEP 609, Applicants request that a copy of the 1449 form, initialed as being considered by the Examiner, be returned to the Applicants with the next official communication.

Pursuant to 37 C.F.R. §1.97(b), it is believed that no fee or statement is required with the Information Disclosure Statement. However, if an Office Action on the merits has been mailed, the Commissioner is hereby authorized to charge the required fees to Deposit Account No. 19-0743 in order to have this Information Disclosure Statement considered.

The Examiner is invited to contact the Applicants' Representative at the below-listed telephone number if there are any questions regarding this communication.

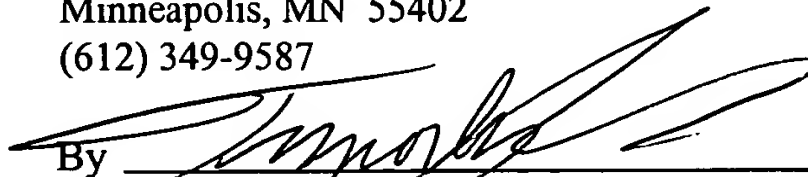
Respectfully submitted,
LEONARD FORBES ET AL.

By their Representatives,
SCHWEGMAN, LUNDBERG, WOESSNER & KLUTH, P.A.
P.O. Box 2938
Minneapolis, MN 55402
(612) 349-9587

Date

17 Aug '04

By



Timothy B Clise
Reg. No. 40,957

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Nicole Jack
Name

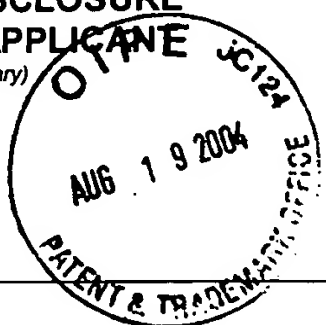

Signature

Under the Paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it contains a valid OMB control number.

Substitute for form 1449A/PTO

**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

(Use as many sheets as necessary)



Complete if Known

Application Number	09/945,507
Filing Date	August 30, 2001
First Named Inventor	Forbes, Leonard
Group Art Unit	2824
Examiner Name	Dinh, Son

Sheet 1 of 1

Attorney Docket No: 1303.014US1

US PATENT DOCUMENTS

Examiner Initial *	USP Document Number	Publication Date	Name of Patentee or Applicant of cited Document	Class	Subclass	Filing Date If Appropriate
	US-4,688,078	08/18/1987	Hseih, Ning	257	321	12/11/1985
	US-4,939,559	07/03/1990	DiMaria, D. J., et al.	257	38	04/01/1986
	US-5,488,612	01/30/1996	Heybruck, W. F.	714	725	10/04/1993
	US-5,959,465	09/28/1999	Beat, R.	326	39	12/29/1995
	US-6,377,070	04/23/2002	Forbes, Leonard	326	41	02/09/2001
	US-6,514,842	02/04/2003	Pral, K. D., et al.	438	593	08/08/2001
	US-6,730,575	05/04/2004	Eldridge, Jerome M.	257	310	08/30/2001

FOREIGN PATENT DOCUMENTS

Examiner Initials*	Foreign Document No	Publication Date	Name of Patentee or Applicant of cited Document	Class	Subclass	T ²
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OTHER DOCUMENTS -- NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
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EXAMINER**DATE CONSIDERED**

Substitute Disclosure Statement Form (PTO-1449)

* EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.¹ Applicant's unique citation designation number (optional) ² Applicant is to place a check mark here if English language Translation is attached